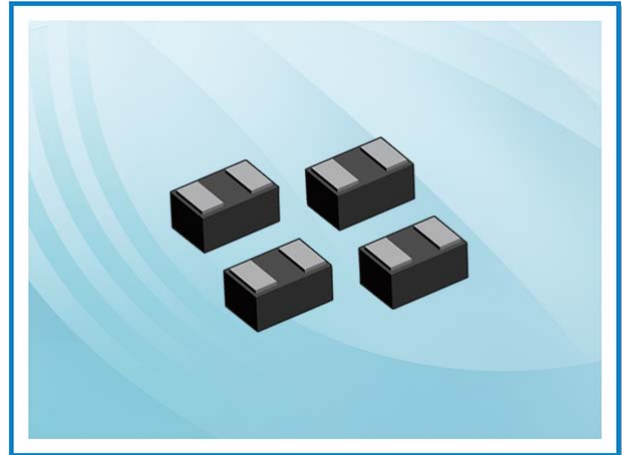


PTLC0521NS – ESD Protection Diode

Feature

- 40 Watts peak pulse power (8/20μs)
- Tiny DFN0603 package
- Bidirectional configurations
- Solid state silicon-avalanche technology
- Low clamping voltage
- Low leakage current
- Low capacitance (C_j = 2.7 pF typ.)
- Protect one data/power line
- IEC61000-4-2 (ESD) ±15kV (Air), ±10kV (Contact)
- IEC61000-4-4 (EFT) 40A (5/50ns)
- IEC61000-4-5 (Lightning): 3.5A (8/20μs)



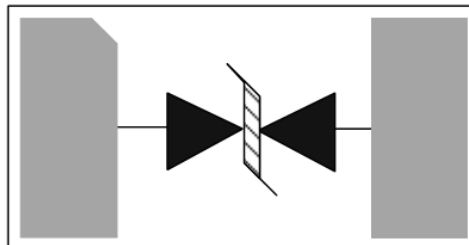
Applications

- Cell Phone Handsets and Accessories
- Micro processor based equipment
- Personal Digital Assistants (PDA's)
- Notebooks, Desktops and Servers
- Portable Instrumentation

Mechanical Data

- DFN0603 package (0201)
- Molding compound flammability rating: UL94 V-0
- Tape and Reel Packaging
- RoHS/WEEE Compliant

Schematic and PIN Configuration



DFN0603

Maximum Rating

Parameter	Symbol	Limit	Unit
IEC61000-4-2 ESD Voltage – Air Mode	V _{ESD} ⁽¹⁾	±15	kV
IEC61000-4-2 ESD Voltage – Contact Mode		±10	
Peak Pulse Power	P _{pp} ⁽²⁾	40	W
Peak Pulse Current	I _{pp} ⁽²⁾	3.5	A
Maximum Lead Solder Temperature (10 seconds duration)	T _L	260	°C
Junction Temperature	T _J	-55~125	°C
Storage Temperature Range	T _{stg}	-55~125	°C

Note:

1. Device stressed with ten non-repetitive ESD pulses.
2. Non-repetitive current pulse 8/20μs exponential decay waveform according to IEC61000-4-5.
3. All ratings are measured at environmental temperature of TA = 25 °C unless otherwise noted.

PTLC0521NS – ESD Protection Diode

Electrical Characteristics

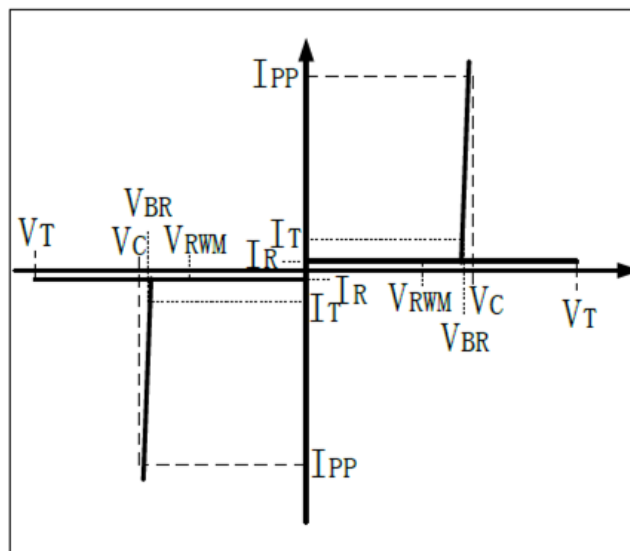
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Reverse Stand-off Voltage	$V_{RWM}^{(1)}$				5.0	V
Reverse Breakdown Voltage	V_{BR}	$I_T = 1\text{mA}$	5.5	6.5	8.5	V
Trigger Voltage	V_T		12		18	V
Reverse Leakage Current	I_R	$V_{RWM} = 5\text{V}$		0.1	0.5	μA
Peak Pulse Current	I_{PP}				3.5	A
Clamping Voltage	$V_C^{(2)}$	$I_{PP} = 3.5\text{A}$			11	V
Junction Capacitance	C_J	$V_R = 0\text{V}, f = 1\text{MHz}$		2.7	3.5	pF

Note:

1. Other voltages available upon request.
2. Non-repetitive current pulse 8/20 μs exponential decay waveform according to IEC61000-4-5.
3. All ratings are measured at environmental temperature of $T_A = 25^\circ\text{C}$ unless otherwise noted.

Electrical Parameters

Symbol	Parameter
V_C	Clamping Voltage @ I_{PP}
I_{PP}	Peak Pulse Current
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
I_R	Reverse Leakage Current @ V_{RWM}
V_{RWM}	Reverse Stand-off Voltage
V_T	Trigger Voltage



PTLC0521NS – ESD Protection Diode

Typical Characteristics

Figure 1: Peak Pulse Power vs. Pulse Time

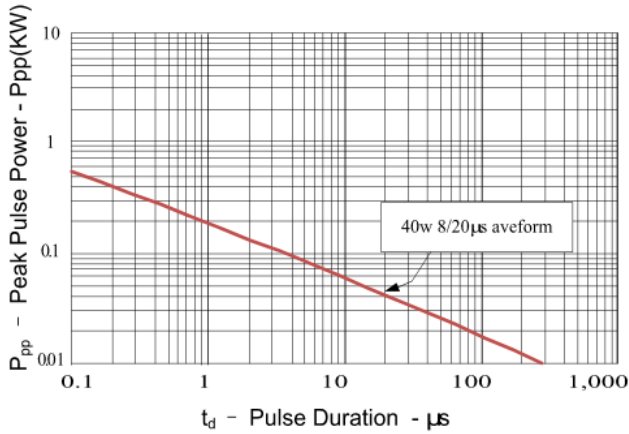


Figure 2: Power Derating Curve

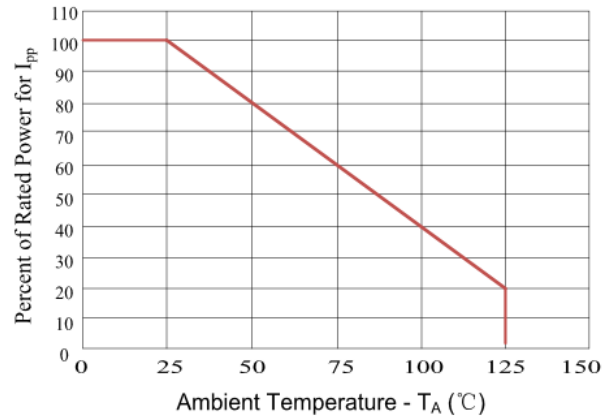


Figure3: Pulse Waveform

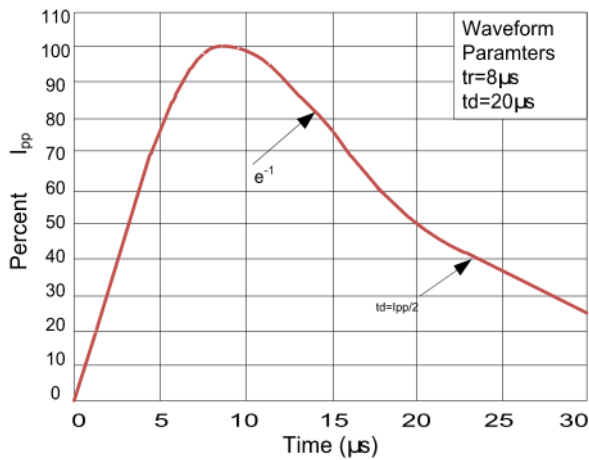
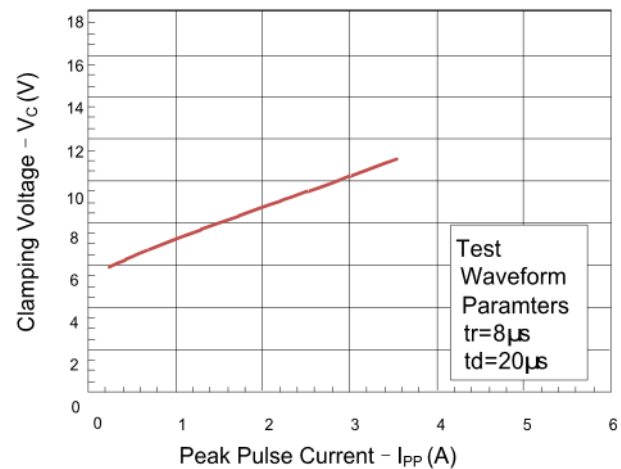
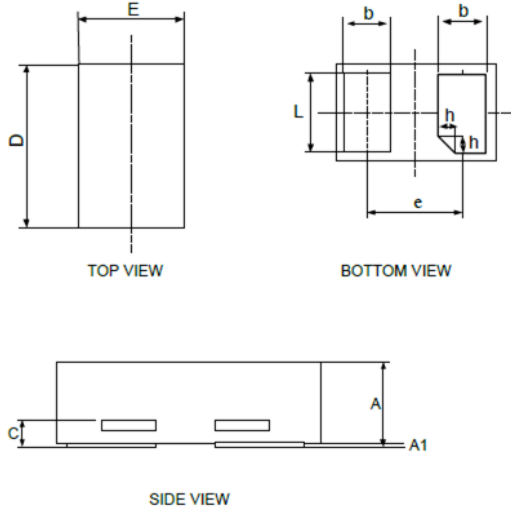


Figure 4: Clamping Voltage vs. Ipp



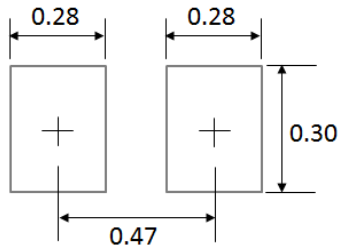
PTLC0521NS – ESD Protection Diode

DFN0603 Package Outline Dimensions

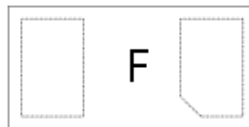


Symbol	Dimensions (mm)		
	Min	Typ	Max
A	0.28	0.30	0.32
A1	0.00	0.02	0.05
C	0.05	0.10	0.15
D	0.55	0.60	0.65
E	0.25	0.30	0.35
b	0.14	0.19	0.24
L	0.20	0.25	0.30
h	0.00	0.05	0.10
e	0.40 BSC		

DFN0603 Recommended Pad Layout



Marking



Packaging Information

Order Code	Packaging	Reel Size	PCS/Reel
PTLC0521NS	DFN0603	7 inch	15,000